

ROBERT JAMES ROSS

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CAREER SUMMARY

- Current employment as an electronic hardware engineer specialising in high speed FPGA design
- Achieved Bachelor of Computer Science (1st Hons) and Bachelor of Electronic Engineering (1st Hons)
- Awarded best (Year 11) debater in Victoria (2000) by the Debating Association of Victoria
- Awarded best thesis and 96% for final year engineering project based around a UAV implementation
- Received exceptional marks for both software and hardware university projects
- Worked as a valued team member for almost 8 years in a technical service oriented retail environment

EDUCATION

Tertiary
2002 – 2006

Bachelor of Computer Science (First Class Hons)
Bachelor of Electronic Engineering (First Class Hons)
La Trobe University

Have demonstrated exceptionally high academic achievement, whilst completing the course under a 15% overload. Achieved a Computer Science honours grade of 83.86% and an Engineering honours grade of 91.25%, demonstrating high academic achievement for all tertiary of study.

Secondary
1999 – 2001

Victorian Certificate of Education
Plenty Valley Christian School

Final Year Subjects:

English, Specialist Mathematics, Mathematical Methods, Further Mathematics, Physics, Chemistry, Information Processing and Management.

Positions Held:

Class Debating Captain - 2000

School Debating Captain - 2001

Student Representative Council Member - 2001

School Chess Team Captain – 1999

DEMONSTRATED SKILLS

Written and communication skills:

Awarded best final year engineering thesis and excelled in written communication throughout numerous workplaces

Established excellent oral communication skills through years of interschool debating and oral presentations.

Organisational and Teamwork skills: Developed good management skills and an ability to work capably under pressure as demonstrated from my academic achievements and extensive work experience. Able to work efficiently and cooperatively in a fast-paced team environment as demonstrated from my part-time work in customer service

Technical skills: Experienced in circuit prototyping and testing techniques including: soldering (SMT and through-hole), wire-wrap and bread-boards. Experienced in PCB design and fabrication. Utilized microprocessors and FPGA's from a variety of vendors including: Microchip, Motorola, Atmel, Zilog, Xilinx and Altera

Computer skills: Competent in use of all Microsoft Office applications, Windows platforms and DOS, Familiar with Scripting, Batch processing and LINUX Proficient in use of C/C++, VB, VHDL, PHP, Also familiar with: various assembly languages, HTML, Matlab, PSPICE, Latex, Perl and Scheme

EMPLOYMENT HISTORY

Date: December 2006 – Current
 Organisation: NEC Australia
 Position: Graduate Hardware Engineer
 Responsibilities: Member of the FPGA design team, developing communications equipment for internet and broadband technologies.

Date: December 2004 – February 2005
 Organisation: Tectonica Australia
 Contact: Brett Ross (Engineer – Project Manager)
 Contact Number: 93816700
 Position: Undergraduate Engineer (vacation work experience)
 Responsibilities: Developed a fleet management and asset tracking system to enable fleet services to better manage their vehicles. System integrated Bluetooth, GPS, GPRS connecting to an information server receiving incoming location information via the GPRS gateway and serving information to clients regarding the status of their fleets.

Date: December 2003-February 2004
 Organisation: Telstra (Network Design and Construction Group)
 Contact: Mr. David Price (Project Manager – Transport Division)
 Contact Number: (03) 8633 1529
 Position: Undergraduate Engineer (vacation work experience)
 Responsibilities: Developed an expert quotation tool to assist network designers in developing quotes. Designed control code to automatically poll train control systems, collating data in a shared database. This program then communicated with the train control network via a RS-232 protocol. Was selected to travel interstate to design and complete a train management system (based in Western Australia)

Date: February 1999 – 2006
Organisation: Dick Smith Electronics: (Greensborough Store)
Contact: Mr Dimitri Zachoropolous (Sales Manager)
Contact Number: 94325125
Position: Casual Sales Assistant
Responsibilities: Conducted myself professionally to serve customers and solve their problems
Provided technical consultation.
Achieved exceptional standards in both store presentation and customer service.

PROFESSIONAL MEMBERSHIP

May 2003 - current The Institution of Engineers, Australia
April 2003 - current Association of Professional Engineers, Scientists and Managers

VOLUNTEER EXPERIENCE

Organisation: Eltham Presbyterian Church
Date: August 2003 – current
Activities: Web site creation, technology operations
Responsibilities:

- Designed and constructed a church website.
- Developed and carried out technology upgrade plan, including LCD projector acquisition and installation.

SIGNIFICANT PROJECTS

Organisation: NEC Australia
Date: 2006 - Current
Description: FPGA hardware designer on a 4GPON high-speed broadband line card in conjunction with NEC Japan.

Organisation: La Trobe University
Date: 2007 - Current
Description: Chief mechanical designer and electronic supervisor for the ROV (Remote Operated Vehicle) submarine project to be run in 2008. (www.rross.com.au/rov.html)

Organisation: La Trobe University
Date: 2006
Description: Chief hardware designer for the UAV (Unmanned Aerial Vehicle) engineering project, completed as a final year engineering project. (www.rross.com.au/uav.html)

Organisation: Tectonica
Date: 2004-2005
Description: Conducted hardware and software development for a GPS based fleet management and virtual warehousing system.

Organisation: Telstra Australia (NDCG)
Date: 2003-2004
Description: Completed software design and created integrated databases for the QOS statistics module of a multi-million dollar services project for WestRail.

ACCREDITATIONS / AWARDS RECEIVED

2006	Best Final year engineering thesis
2005	Dean's Honours Award
2004	Dean's Honours Award Recognition from the Golden Key International Honour Society (top 15% of students) Awarded Best Technical solution for the Employability Skills Challenge
2003	Recognition from the Golden Key International Honour Society
2002	Dean's Honours Award Recognition from the Golden Key International Honour Society
2000	Awarded a Swannie award as the best year 11 debater in Victoria from the Debating Association of Victoria.

PAPERS PUBLISHED

2006	Ross, R., Hall, R. <i>A FPGA simulation using asexual genetic algorithms for integrated self-repair</i> . In the Proceedings of the 2006 Conference on Adaptive Hardware and Systems, June 2006
2006	Ross, R., Hall, R. <i>Towards self-configuring evolvable FPGA using feedback cross-checking</i> . In Proceedings of the International Conference on Knowledge-Based & Intelligent Information & Engineering Systems, October 2006.

INTERESTS

Reading, Problem Solving, Chess, Programming, Public Speaking, Debating, Model Aviation

REFEREES (AVAILABLE UPON REQUEST)
